

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger
Assignee: Xilinx, Inc.
Title: "A Programmable Logic Device with Time-Multiplexed Interconnect"
Serial No.: Not Yet Known File Date: 03-26-04
Examiner: Not Yet Known Art Unit: Not Yet Known

Divisional of
Serial No.: 10/253,740 File Date: 09-23-02
Docket No.: X-1006-1D US

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INFORMATION DISCLOSURE STATEMENT

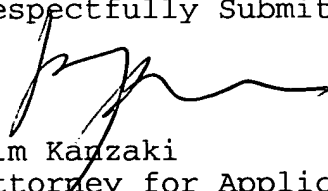
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the six (6) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 10/253,740 filed September 23, 2002 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,


Kim Kanzaki
Attorney for Applicant
Reg. No. 37,652

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Examiner Signature		Date Considered	
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